

102a 102b 103a 103b 104a 104b 105a 105b 106a 106b 107a 107b 108a 108b 110a 110b 111a 111b 112a 112b 113a 113b 114a 114b 115a 115b 116a 116b 118a 118b 119a 119b 120a 120b 121a 121b 122a 122b 123a 123b 124a 124b 125a 125b

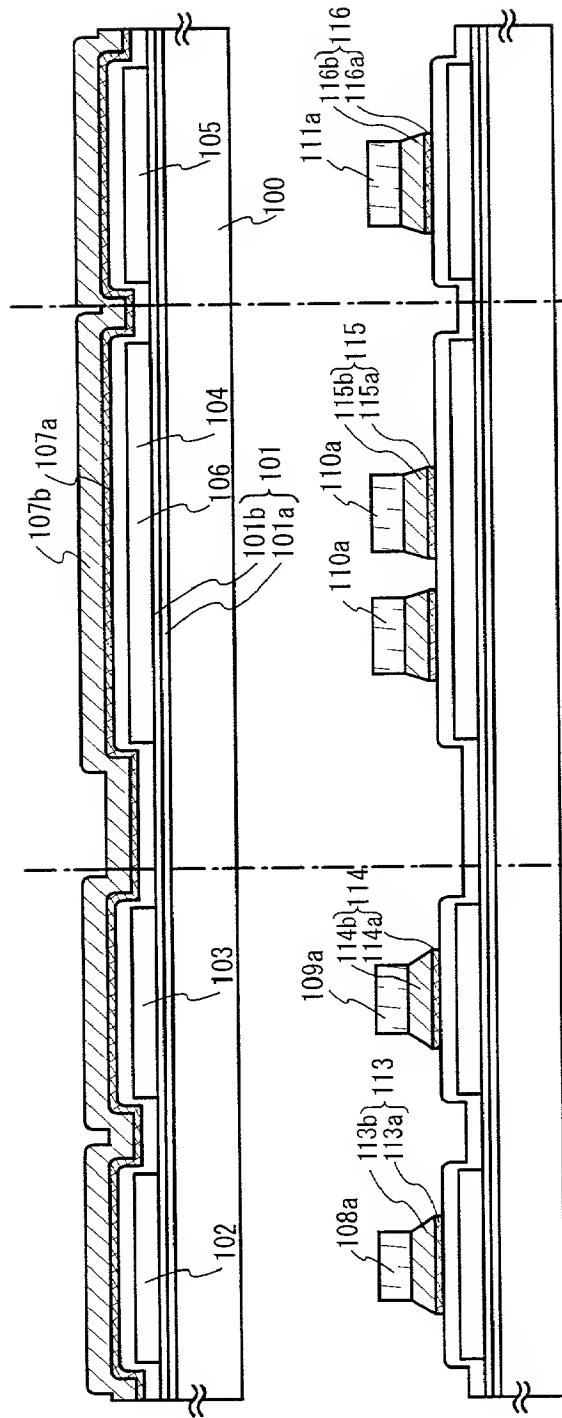


FIG. 1B

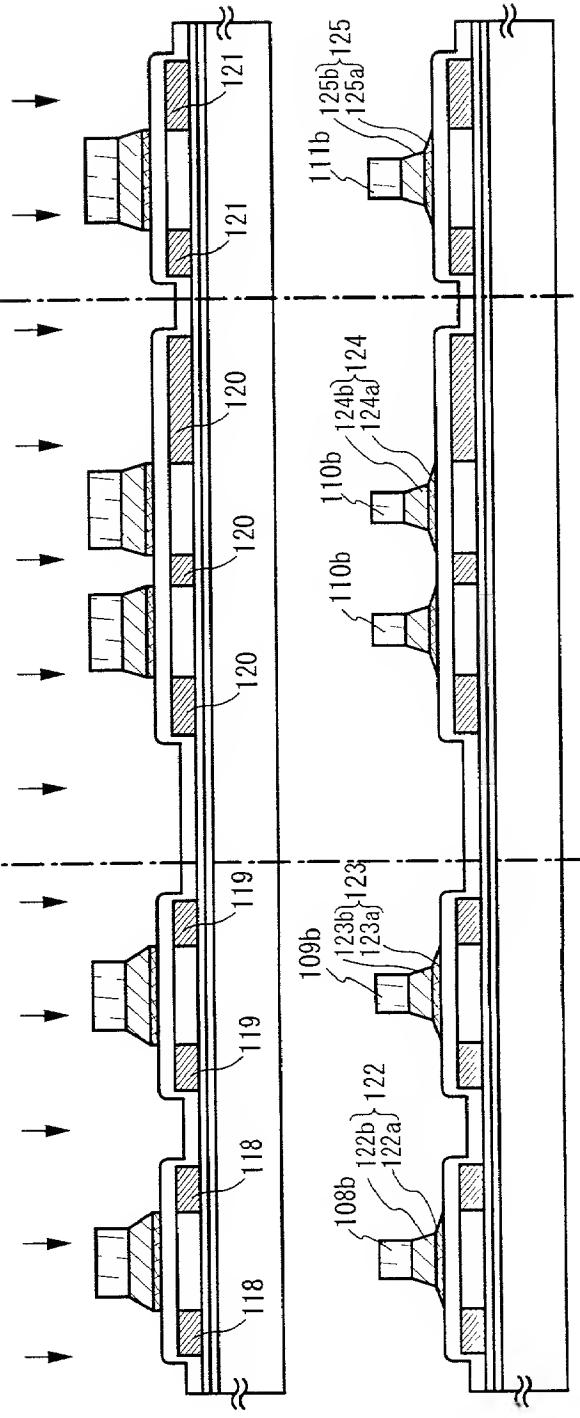


FIG. 1D

137 127 128 138
139 129 130 140
141 131 132 142
133 134 143
144 135 136 145

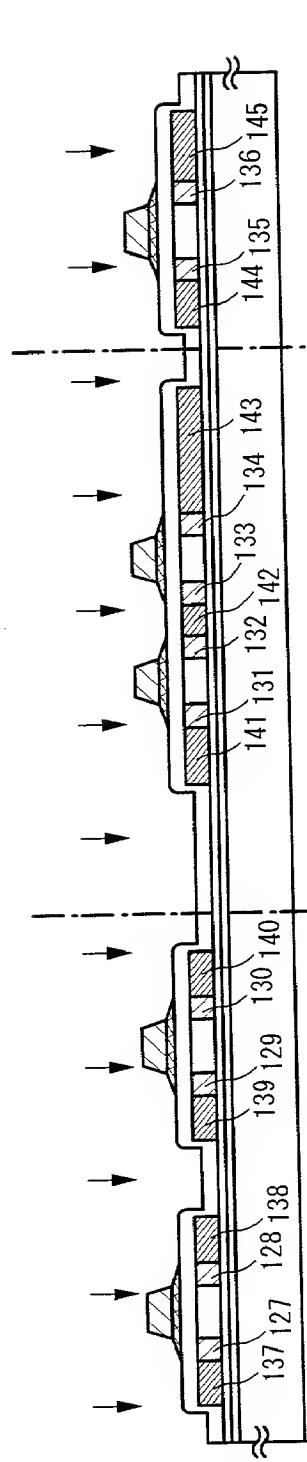


FIG. 2A

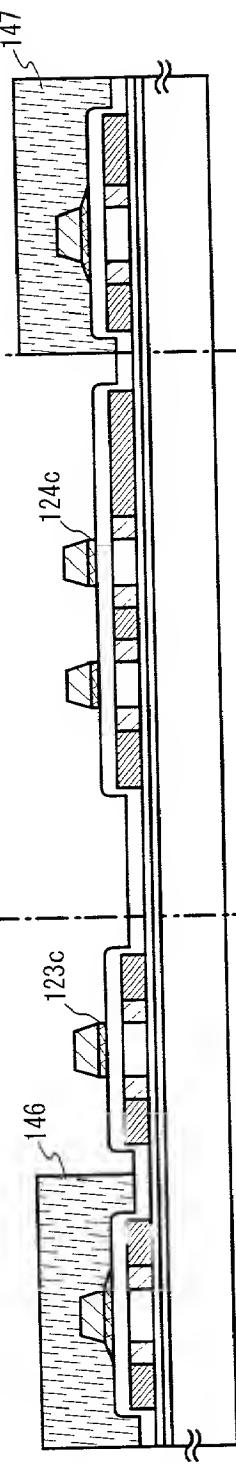


FIG. 2B

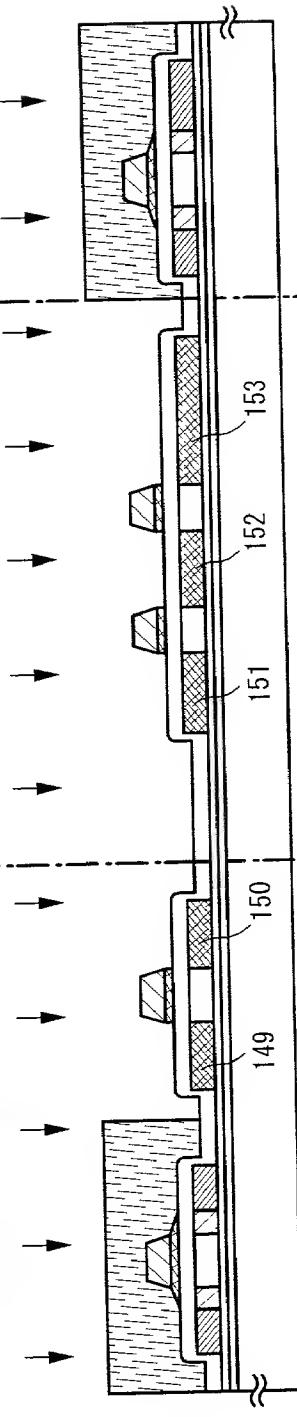


FIG. 2C

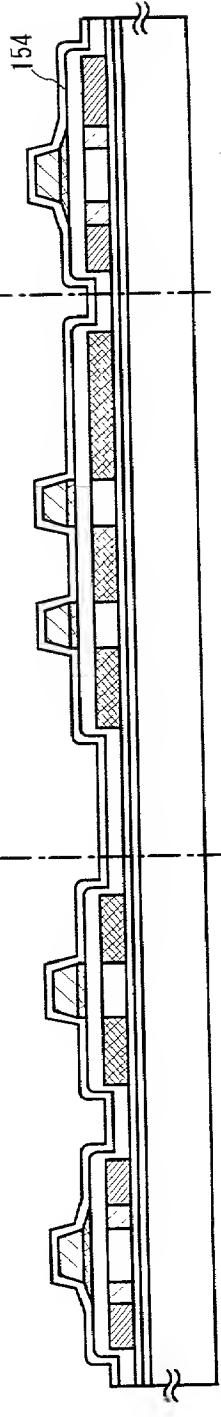


FIG. 2D

200: DRIVING CIRCUIT
201: DRIVER CIRCUIT
202: CMOS CIRCUIT
203: TFT
204: TFT
205: PIXEL PORTION
206: PIXEL TFT
207: STORAGE CAPACITOR
121: SOURCE WIRING
122: GATE WIRING
123: GATE LINE
124: SOURCE LINE
125: GATE LINE
126: SOURCE WIRING
127: GATE WIRING
128: GATE LINE
129: SOURCE LINE
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191: SOURCE WIRING
192: GATE WIRING
193: GATE LINE
194: SOURCE LINE
195: GATE LINE
196: SOURCE WIRING
197: GATE WIRING
198: GATE LINE
199: SOURCE LINE

FIG. 3A

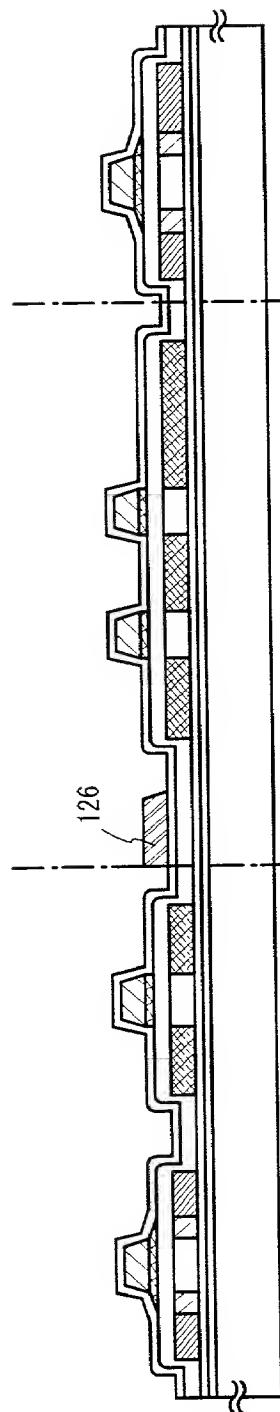
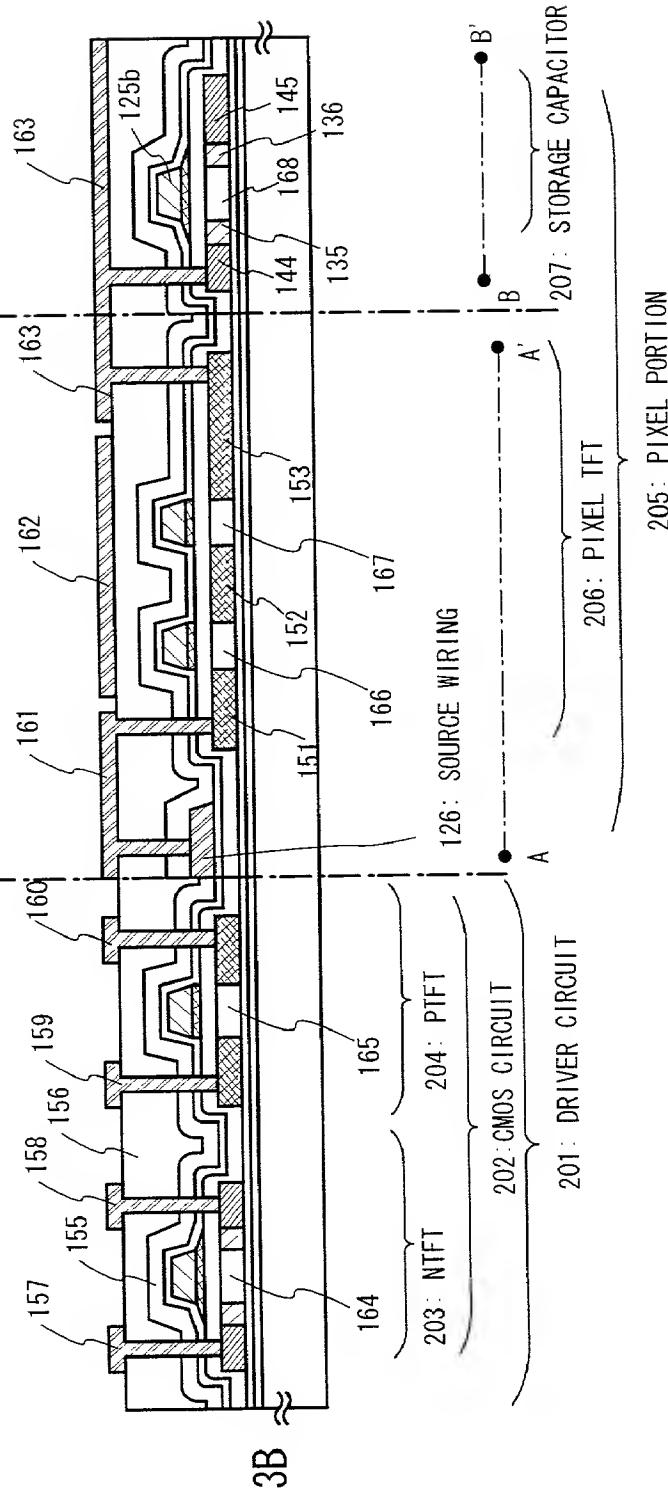


FIG. 3B



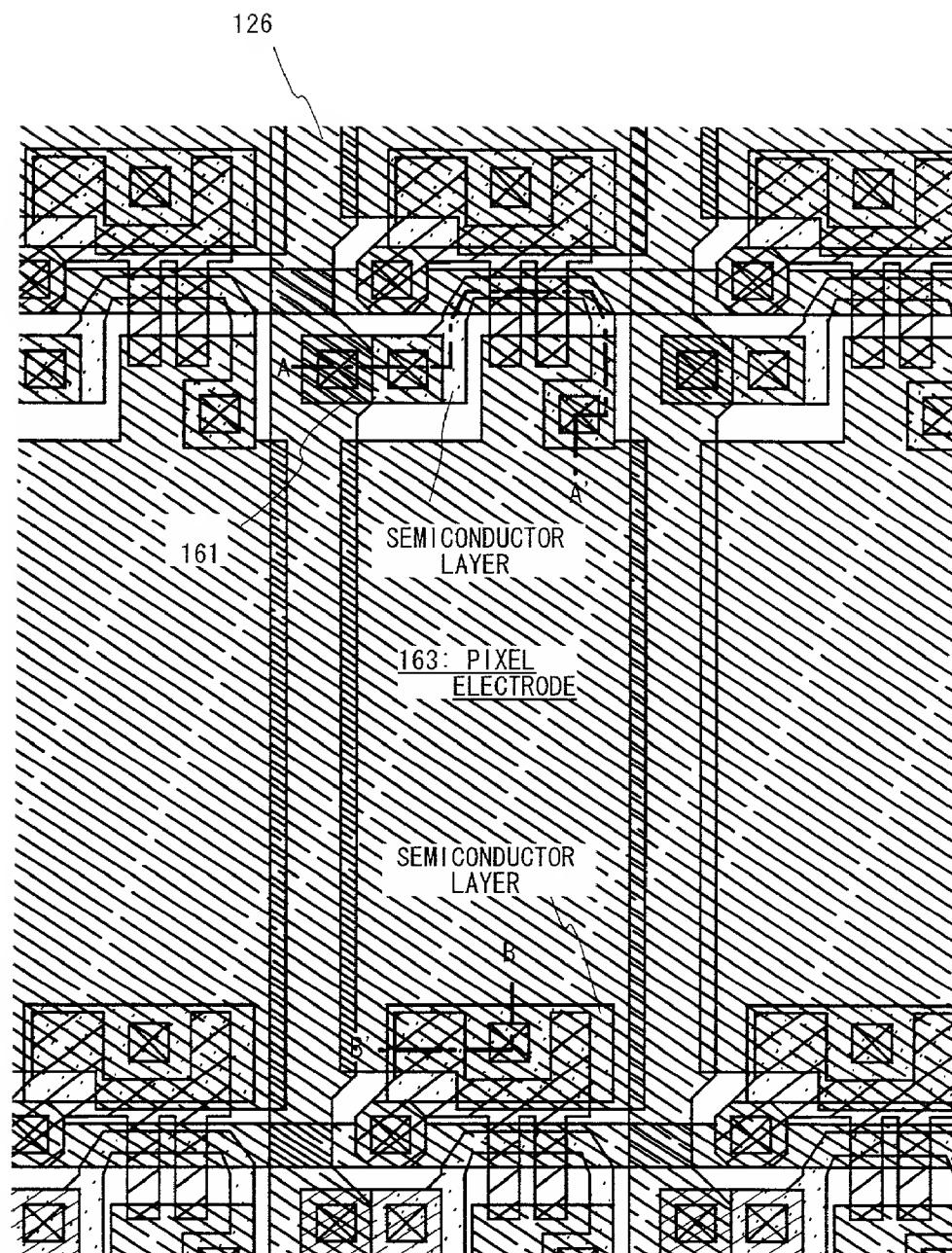


FIG. 4

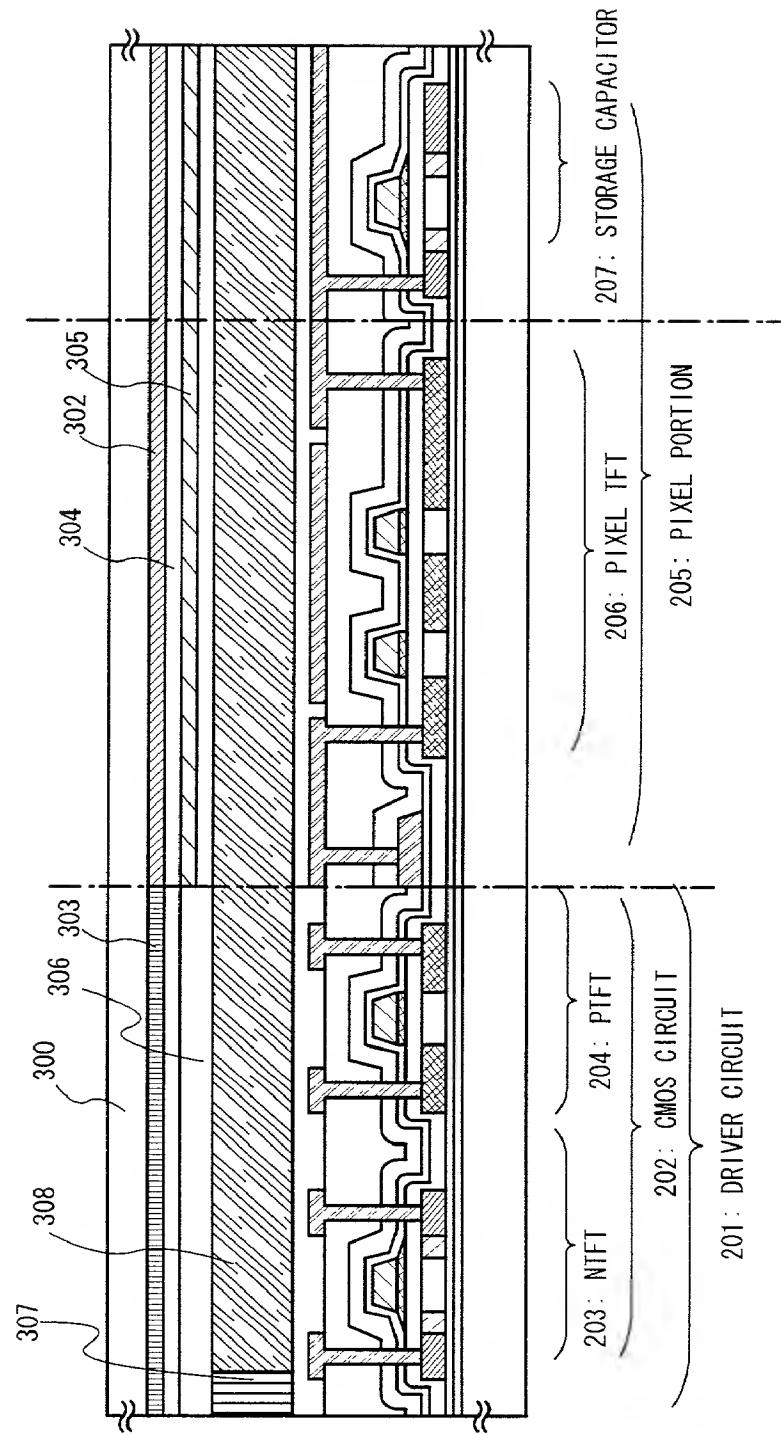


FIG. 5

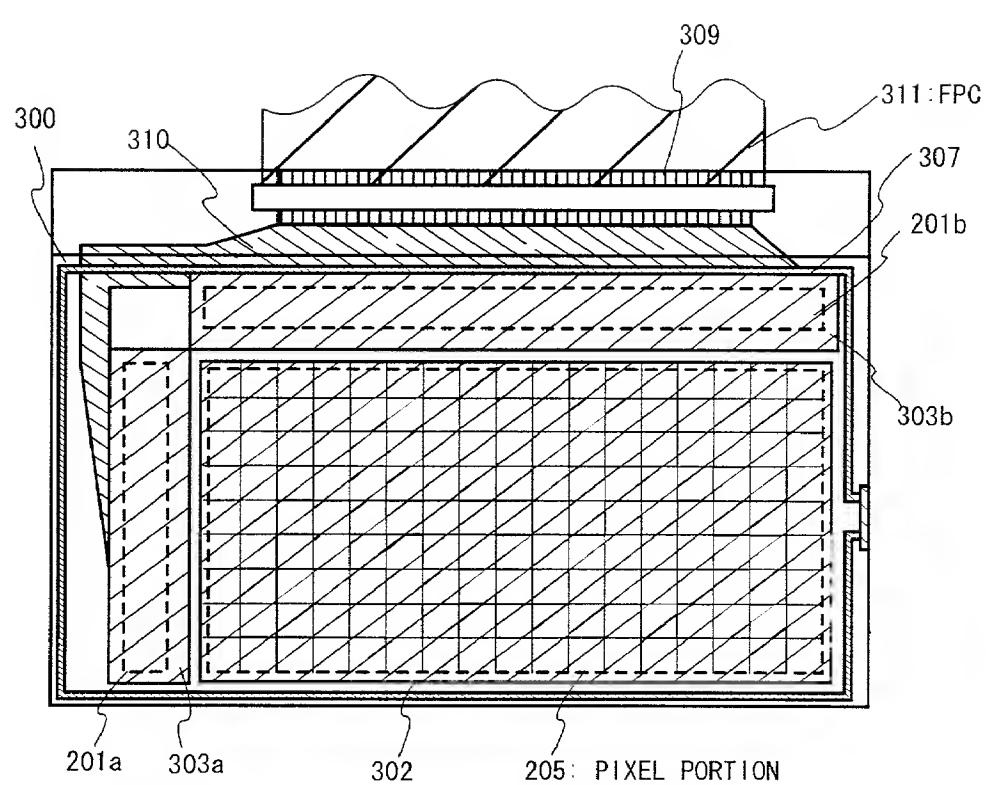


FIG. 6

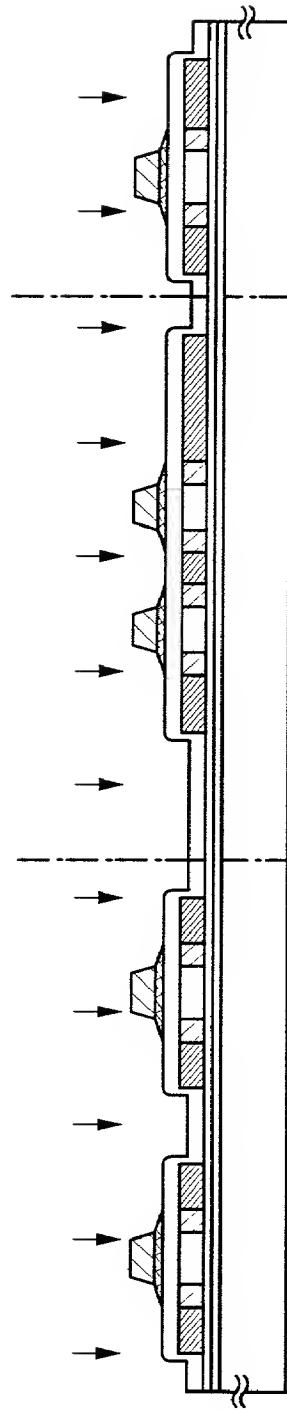


FIG. 7A

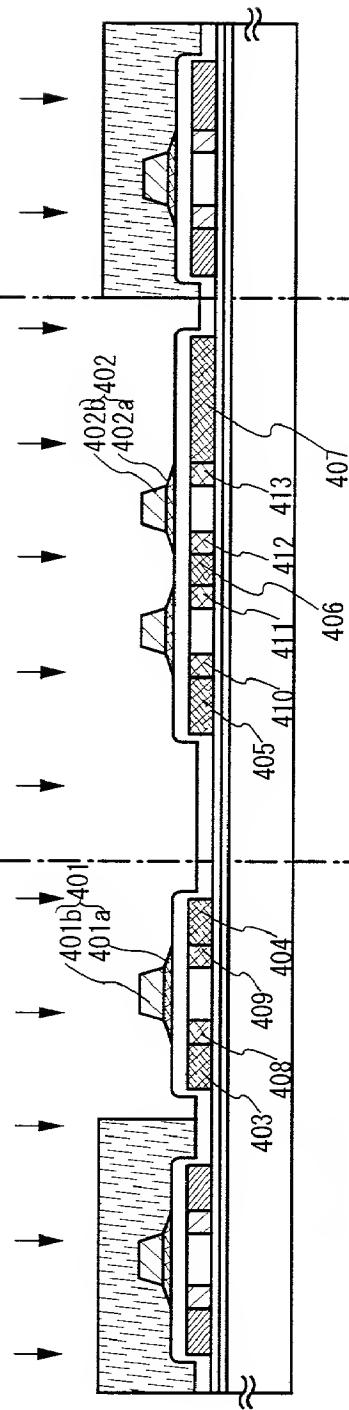


FIG. 7B

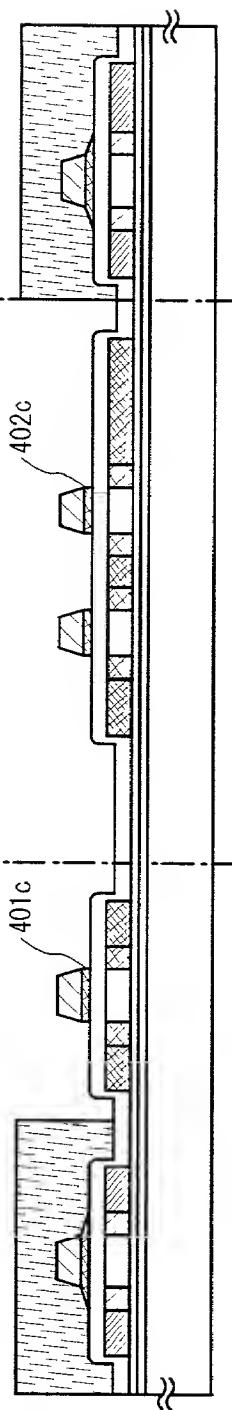
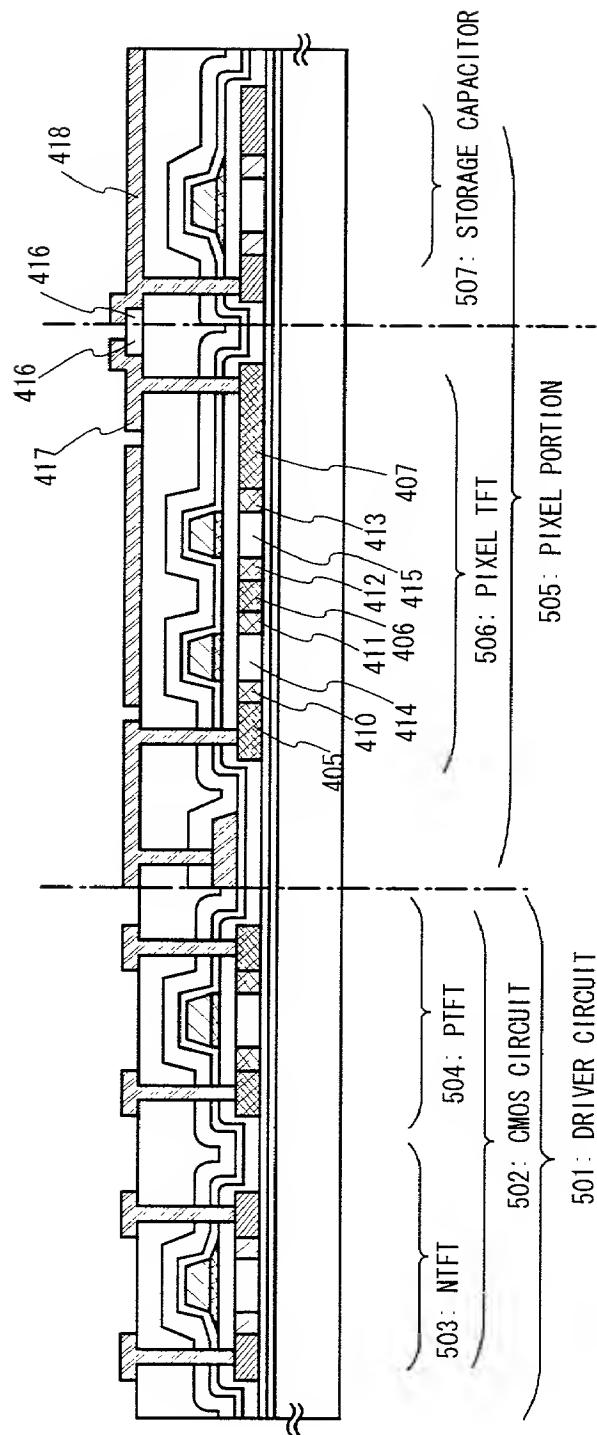


FIG. 7C



8
FIG.

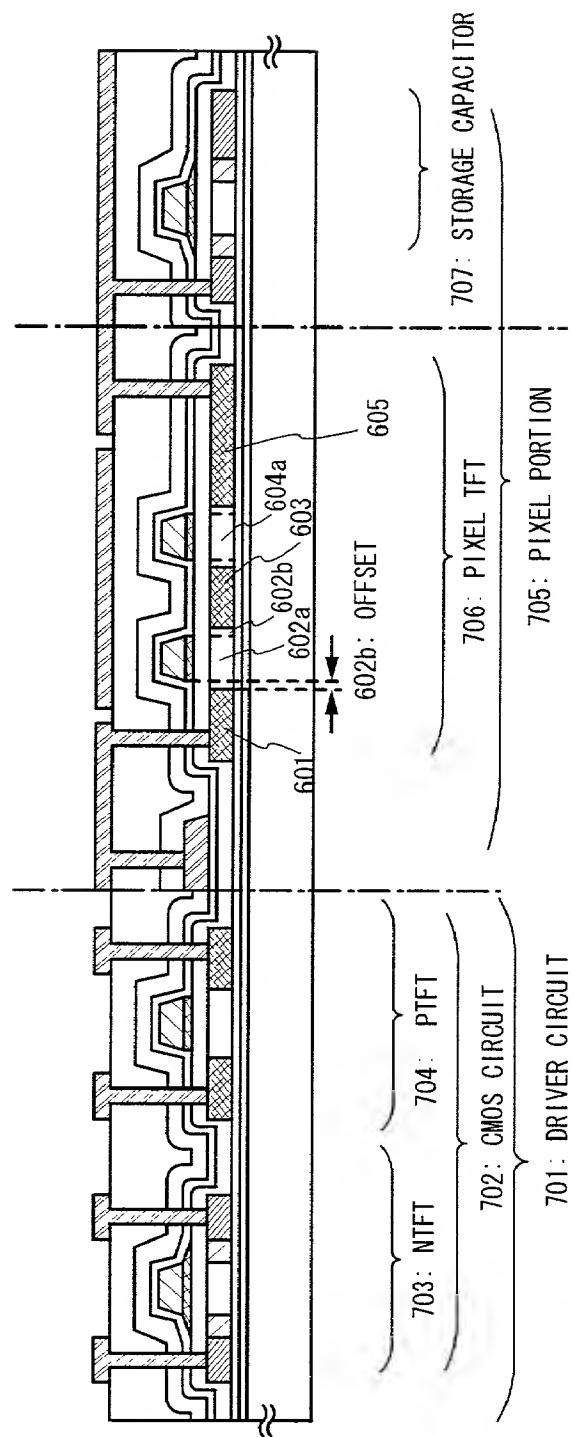


FIG. 9

900: DRIVER CIRCUIT
901: DRIVING CIRCUIT
902: CMOS CIRCUIT
903: TFT
904: PTFT
905: PIXEL PORTION
906: PIXEL TFT
907: STORAGE CAPACITOR

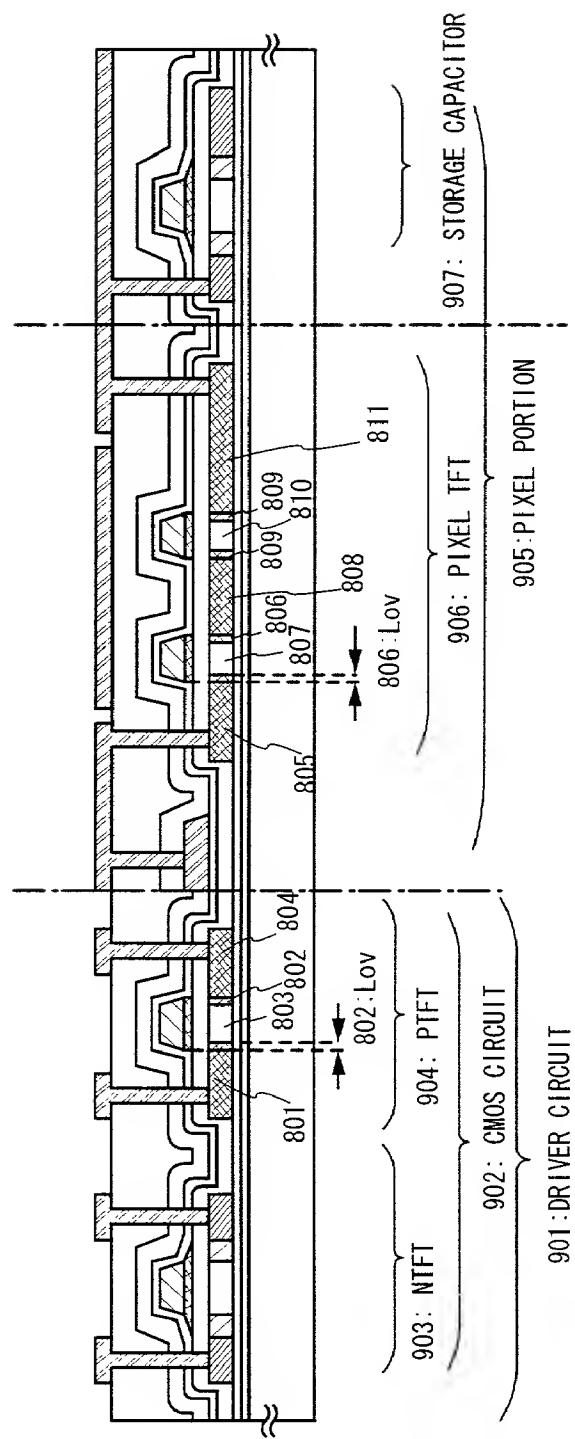


FIG. 10

1101 1102 1103 1104 1105

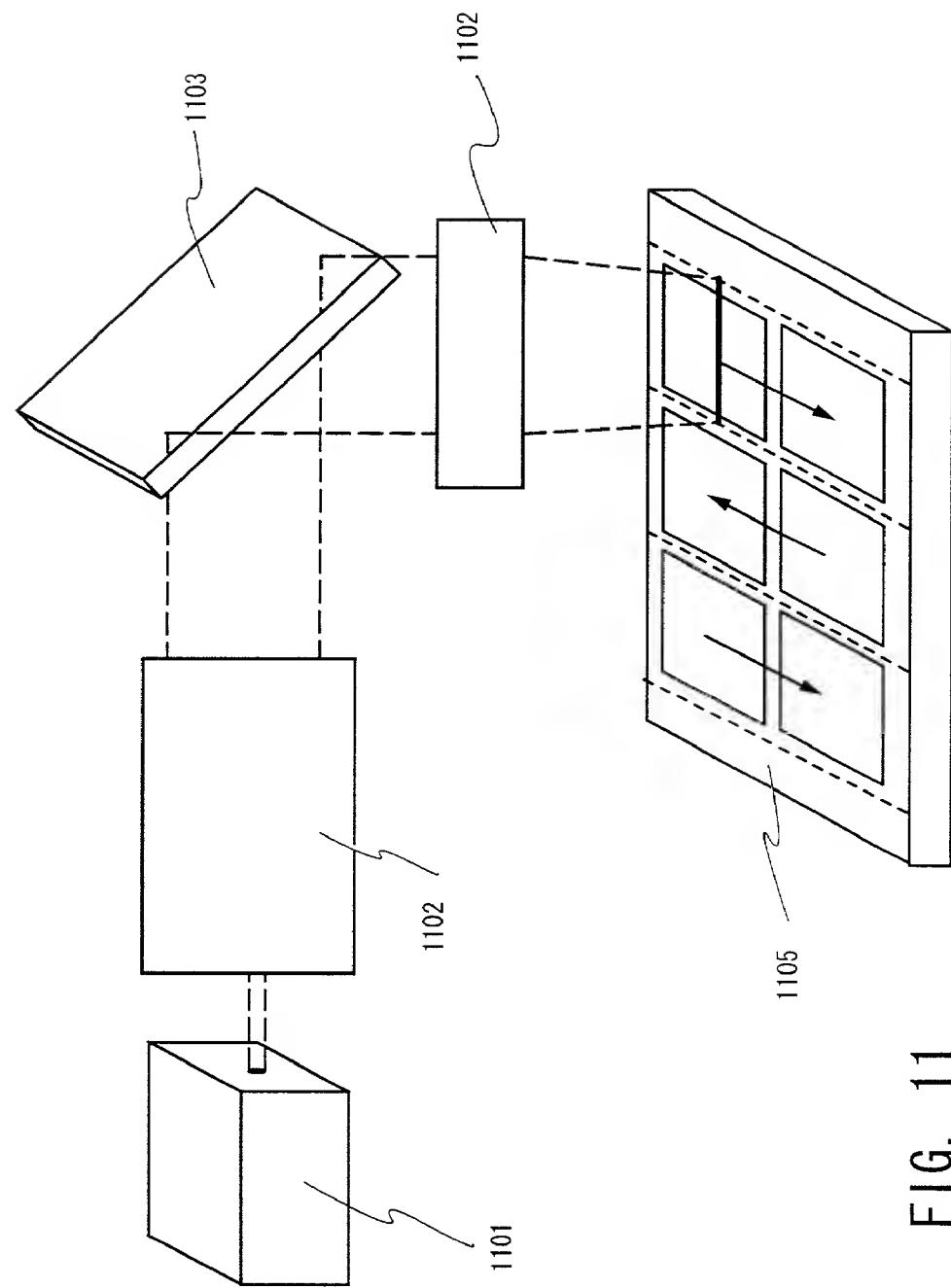


FIG. 11

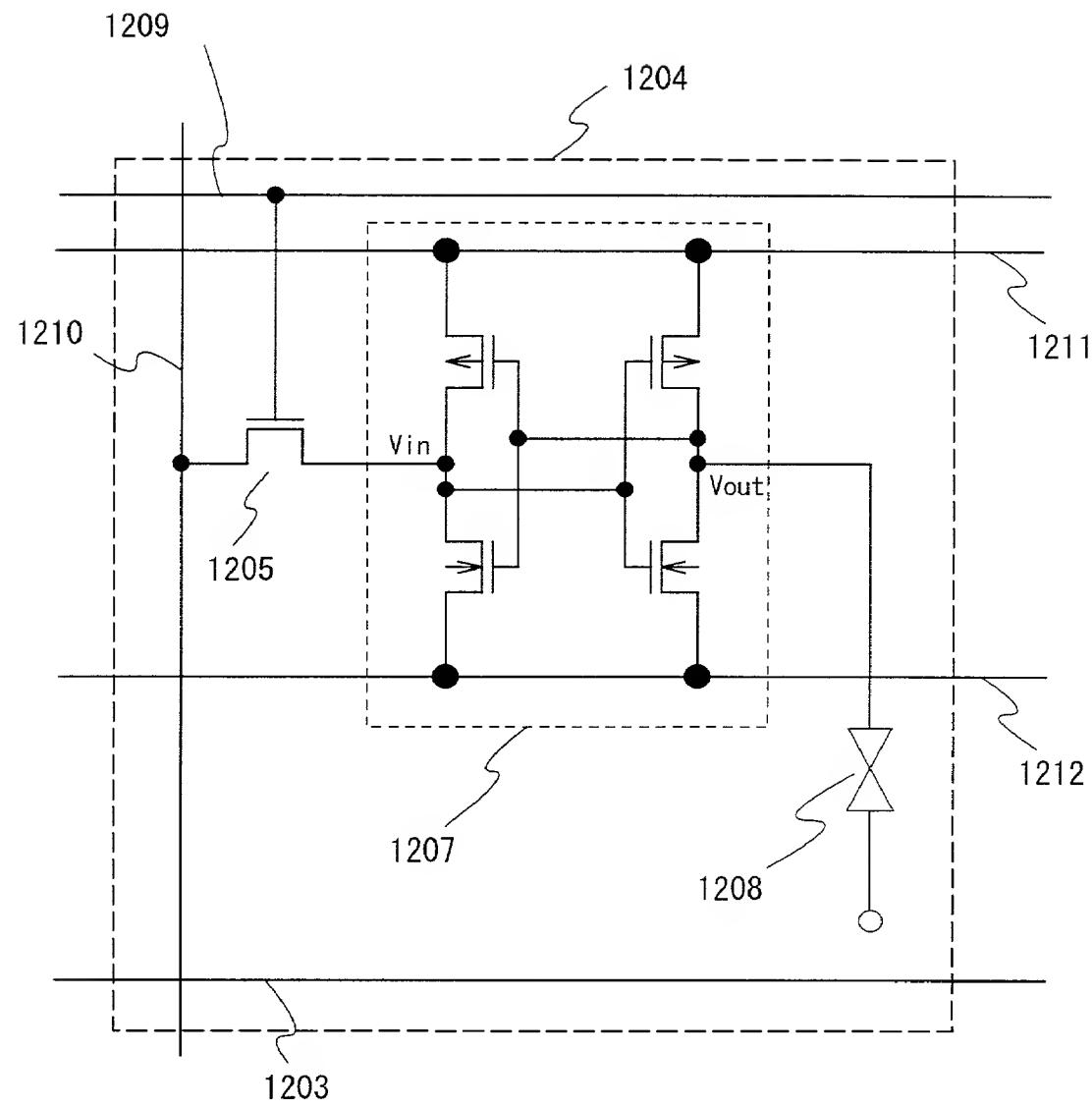


FIG. 12

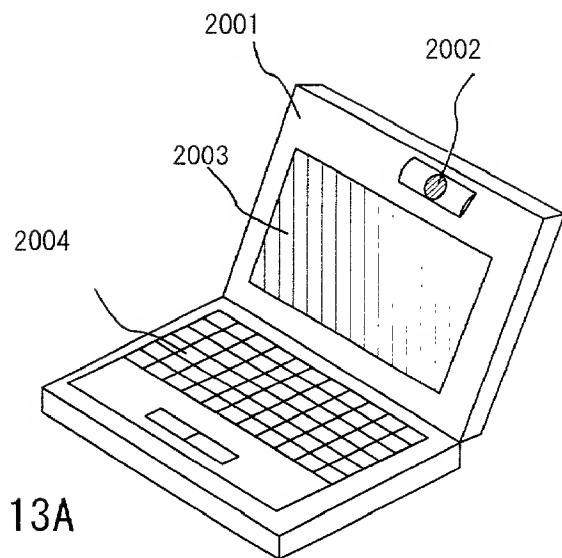


FIG. 13A

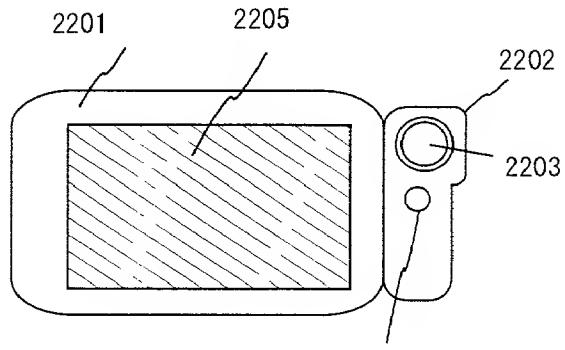


FIG. 13B

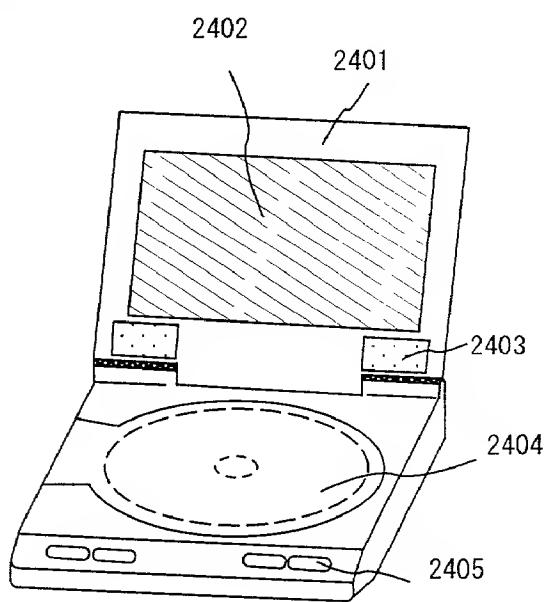


FIG. 13C

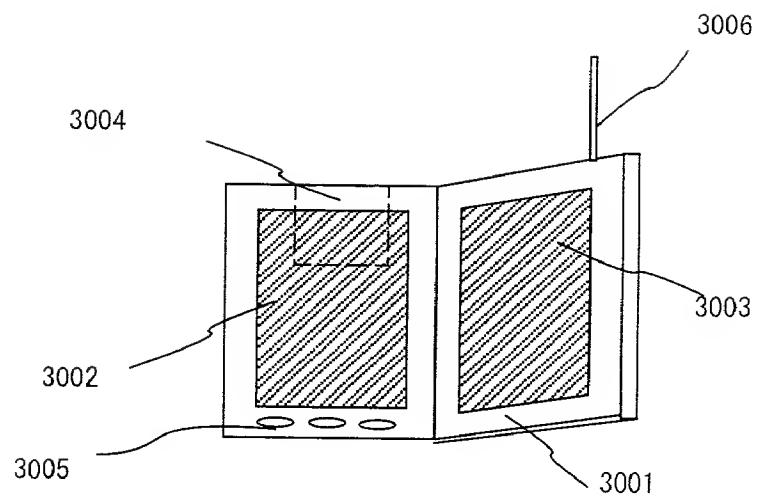


FIG. 14A

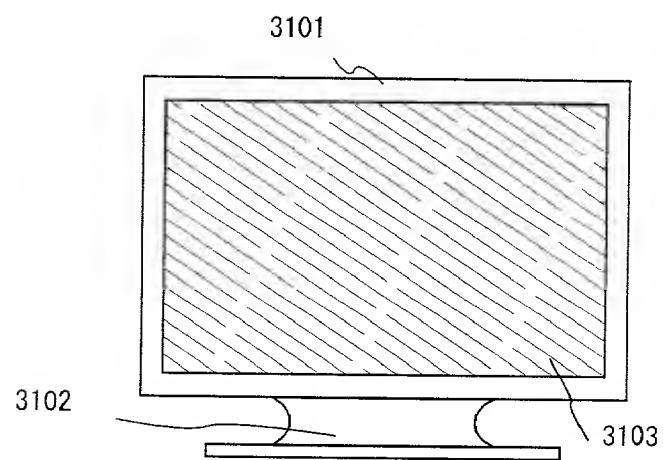


FIG. 14B